



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

S. Babar Raza

Application No.: 10/773,948

Filing Date: February 6, 2004

For: A Device that Provides the
the Functionality of Dual-Ported
Memory Using Single-Ported
Memory for Multiple Clock
Domains

Confirmation No.: 2169

Examiner: Nguyen, Hanh N.

Art Unit: 2616

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO
NON-FINAL OFFICE ACTION

Dear Sir:

In response to the Office Action mailed on July 31, 2007, Applicant respectfully requests reconsideration of the above referenced patent application. Please amend the above-identified application as follows and consider the following remarks for allowance of the above-identified patent application.